RE-EXAMINATION TIMETABLE, MARCH, 2026
BACHELOR OF TECHNOLOGY (B.Tech.)

FINAL YEAR, SEMESTER-VII (For 2025-26 Batch Under Autonomy)
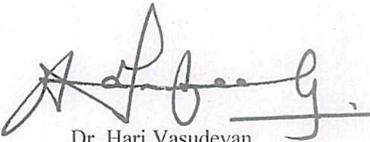
Timings : 10:00 a.m. to 12:00 p.m.

Date: 20th February, 2026

Day & Date		Mechanical Engineering	Electronics & Telecommunication Engineering	Computer Science and Engineering (Data Science)	Computer Engineering	Information Technology
Saturday, 14/03/2026	Honours	-	Deep Learning	-	-	MLOps
	Minors	Advanced Machine Learning	-	-	-	-
Monday, 16/03/2026		Production Planning and Control	Microwave Engineering	Information Security	Internet of Everything	Parallel and Distributed Computing
Wednesday, 18/03/2026		Startup Registration and Development	Artificial Intelligence & Machine Learning	Geo-Spatial Data Science	Deep Learning	-
			Data Compression & Encryption		Predictive Modeling	
Monday, 23/03/2026		Design of Mechanical Systems	Mobile Communication System	Language Models	Natural Language Processing	-
Wednesday, 25/03/2026		-	Personal Finance Management	Management Information System	Personal Finance Management	-
					Research Methodology	
Saturday, 28/03/2026		Bionics Engineering	Internet of Things	-	-	-
		Business Model Design				

Important Note: Changes if any, in the time table shall be communicated on the college website(www.djsce.ac.in)

Ms. Alka Shukla
Controller of Examinations

Dr. Hari Vasudevan
Principal